

# Job Title: Functional Verification Lead

Location: Location: Bengaluru/Bhubaneswar/Ranchi

Experience: 7 - 10 Years

## Technical Requirements:

- **Verification Expertise:** Extensive experience in SoC, sub-system, and block-level verification.
- **Architecture Knowledge:** Hands-on experience with ARM architecture and AMBA protocols at both SoC and sub-system levels.
- **Coding Skills:** Proficient in UVM, OVM, SystemVerilog, Verilog, and C-based verification methodologies.
- **Advanced Techniques:** Exposure to GLS (Gate-Level Simulation) and power-aware verification is an added advantage.
- **Protocol Proficiency:** Working knowledge of protocols such as PCIe, DDRx, Ethernet, USB, AXI, AHB, APB, I2C, and SPI.
- **Debugging & Automation:** Strong RTL debugging skills and experience in test automation scripting using TCL, Perl, or Python.
- **Testbench Development:** Ability to build testbenches and test cases from scratch.
- **Tool Expertise:** Well-versed in industry-standard verification tools and processes.

## Expectations from the Role:

- **Communication & Teamwork:** Strong communication and interpersonal skills, with the ability to work independently or as part of a team.
- **Adaptability:** A fast learner capable of thriving in a distributed work environment.
- **Ownership & Punctuality:** Demonstrates a strong sense of ownership and meets deadlines with punctuality.
- **Leadership:** Ability to mentor and lead a team to solve complex verification challenges.