

Job Title: Functional Verification Engineer

Location: Location: Bengaluru/Bhubaneswar/Ranchi

Experience: 5 - 8 Years

Technical Requirements:

- **Experience:** Proficient in verification at both the sub-system and block levels.
- **Testbench Development:** Hands-on experience in block-level testbench architecture and sub-system level environment integration.
- **Coding Expertise:** Strong skills in UVM, SystemVerilog, and Verilog coding, along with a solid understanding of basic automation methodologies.
- **Protocol Knowledge:** Familiarity with bus protocols such as PCIe, DDRx, Ethernet, USB, AXI, and others is highly preferred.
- **Automation Skills:** Experience in test automation using scripting languages like TCL, Perl, or Python is an advantage.
- **Test Case Development:** Capable of building testbenches and test cases from scratch.
- **Tools Proficiency:** Well-versed in industry-standard verification tools and methodologies.

Expectations for the Role:

- **Communication:** Strong communication and interpersonal skills, with the ability to work both independently and collaboratively within a team.
- **Adaptability:** A quick learner who can thrive in a distributed work environment.
- **Ownership & Punctuality:** Demonstrates a strong sense of ownership and consistently meets deadlines with punctuality.
- **Leadership:** Capable of mentoring and leading a team to solve complex problems